Solid-State Dewetting and Pt-SrTiO₃ Interfaces

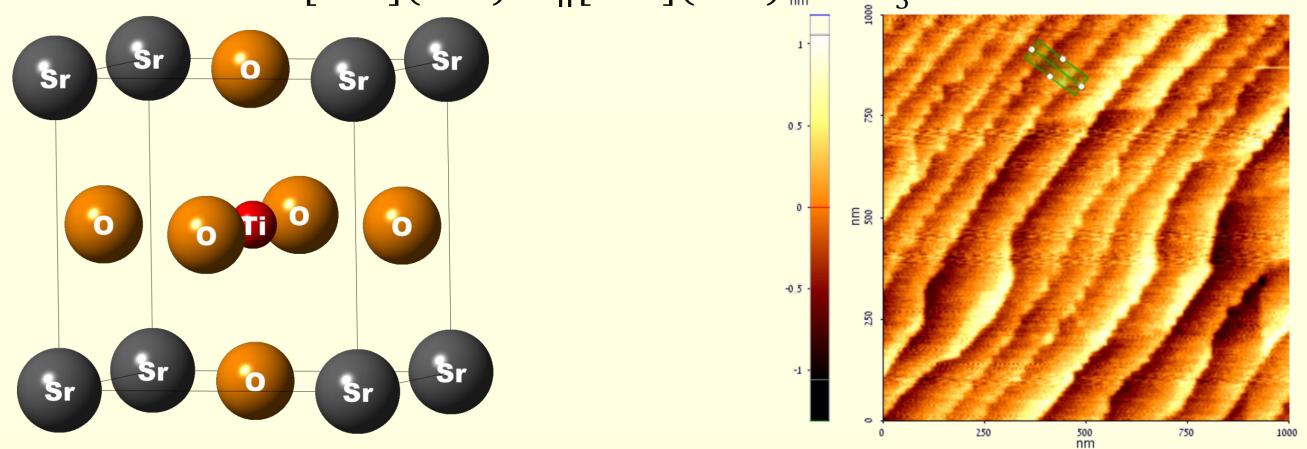
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Introduction

- Solid-state dewetting has become a common method for "bottom-up" processing, such as for producing catalyst particles and nanocrystals (NCs) for memory devices. [1-3]
- *SrTiO*³ *has a cubic perovskite structure and excellent dielectric (band gap ~3.2eV) ferroelectric and optical properties for microelectronics applications.*[4]
- Non-volatile memory (NVM) transistors and capacitors which use Pt nanocrystals (NC) for charge storage shows very good electrical properties such as: memory window, trapped charge density etc.[5]
- ★ $SrTiO_3$ is composed of SrO and TiO_2 atomic planes (figure 1) in the <100> direction, a TiO_2 terminated surface was found to be energetically favorable over a SrO terminated surface [6-7].
- \bullet TiO₂ termination is achieved by BHF solution which selectively dissolved the

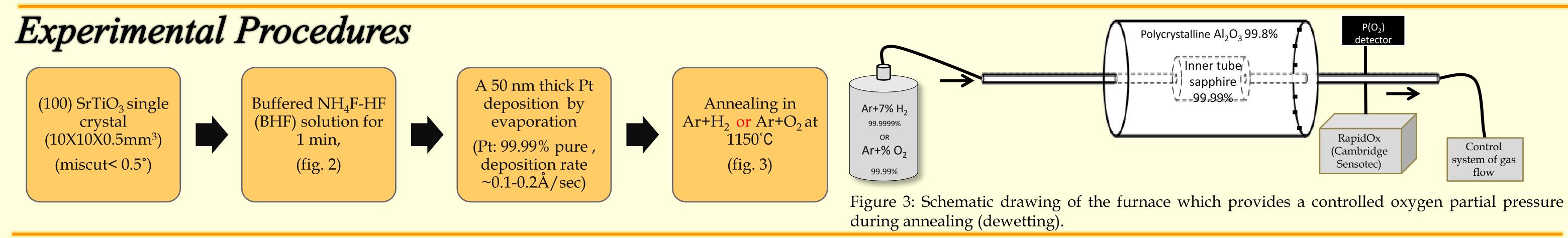
- *Pt-SrTiO₃(100) orientation relationships were reported by A.D. Polli et al.* [8] after annealing in UHV:





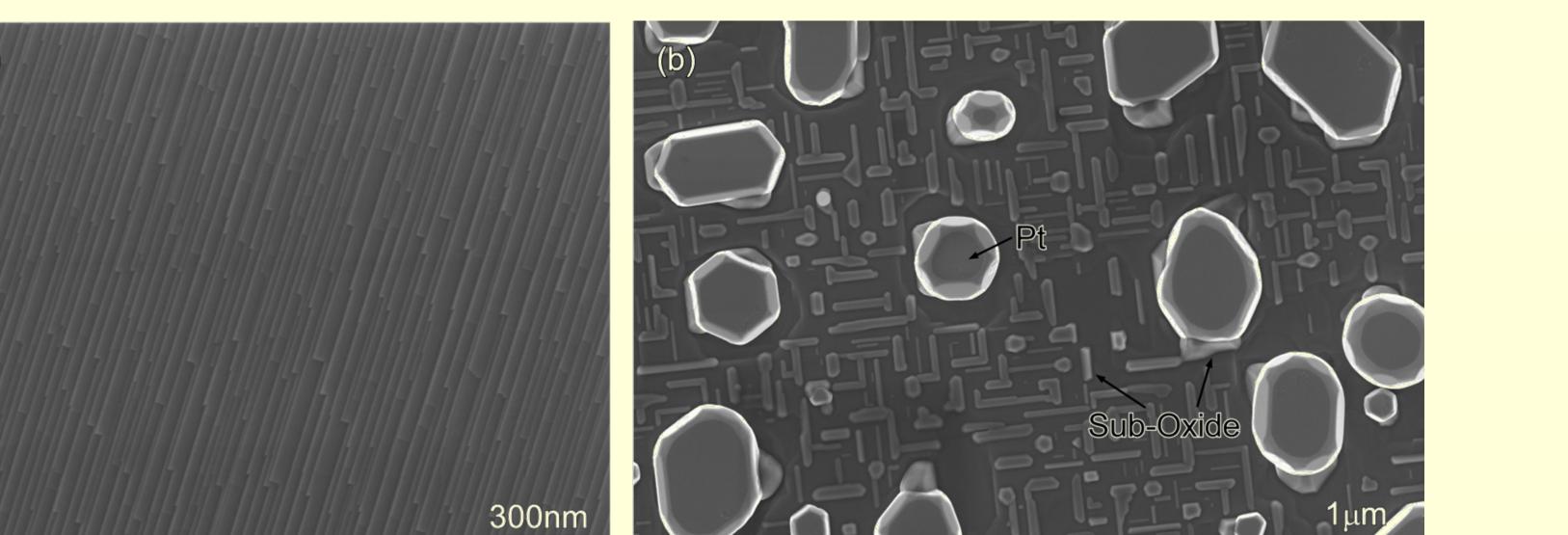
SrO atomic plane followed by high temperature annealing. (figure 2).

Figure 1: Schematic drawing of the $SrTiO_3$ perovskite structure showing the SrO and TiO_2 atomic planes in the [100] direction. Figure 2: AFM image of an BHF etched substrate surface followed by annealing at 950° for 2hr in air.



Results and Discussion

(a)



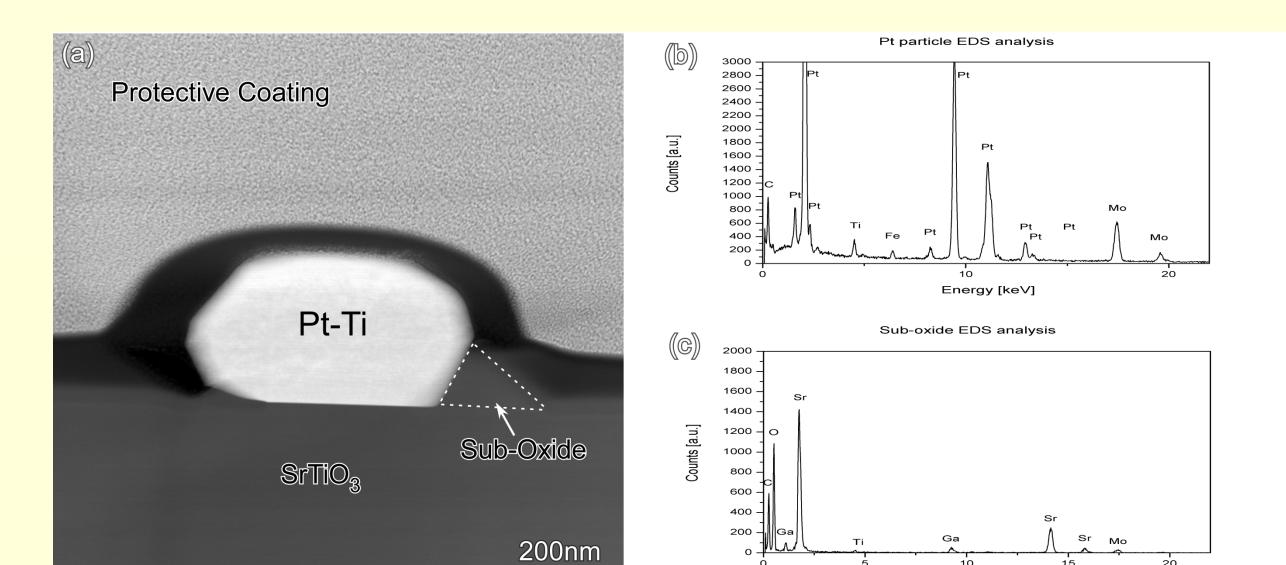
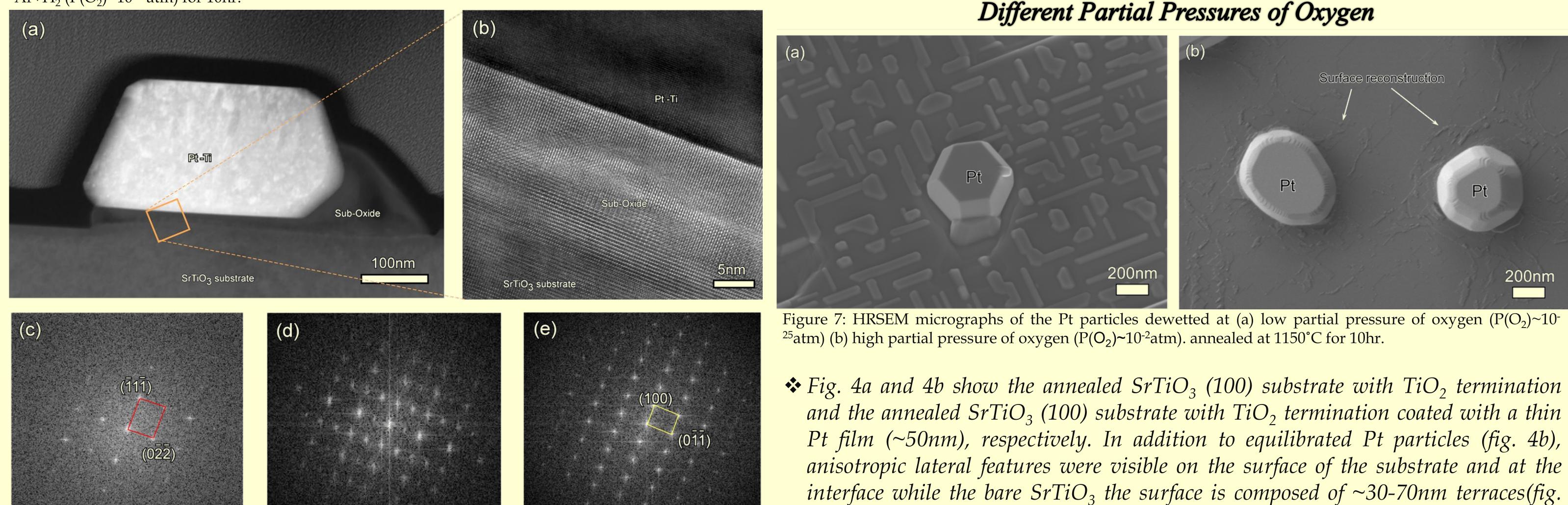


Figure 4: HRSEM micrographs of a (100) SrTiO₃ surface morphology (a) after BHF and annealing at 1150°C in Ar+H₂ (P(O₂)~10⁻²⁵atm) for 2hr (b) dewetted Pt particles and reduced substrate annealed at 1150°C in Ar+H₂ (P(O₂)~10⁻²⁵atm) for 10hr.

Figure 6: (a) HAADF micrograph of a dewetted particle and (b) EDS analysis results show that Pt particles contain a small amount of Ti. (c) EDS analysis from the sub-oxide layer show the presence of only Sr and O.



Pt [211]

Sub-Oxide

STO[011]

Figure 5: (a) HAADF micrograph of a dewetted particle. (b) Cs corrected HRTEM micrograph of the interface between the SrTiO₃ substrate and a Pt particle shows the sub-oxide layer. Corresponding FFT of the *reconstructed exit wave* of (c) Pt particle (d) sub-oxide layer shows tetragonal distortion in [100]SrTiO₃ direction (~8.5%) and additional spotd at the middle of [011]SrTiO₃ direction. (e) SrTiO₃ substrate annealed at 1150°C in Ar+H₂ (P(O₂)~10⁻²⁵atm for 10hr.

- 4a). These results imply that the Pt serves as a catalyst which reduces the SrTiO₃.
 ◆ HRTEM analysis with corresponding FFT of the reconstructed exit wave (fig. 5) show that Pt (111) is parallel to the SrTiO₃ (100) substrate and not Pt(100) as expected due to the small lattice mismatch (~0.4%). This indicates that the interfacial energy of Pt(100) and the sub-oxide is higher than Pt(111).
- These results contradict previous research done by Polli et al. [R] who reported that Pt(100) is parallel to SrTiO₃ (100). The difference can be explained by the different annealing atmospheres and temperatures which probably caused different surface reconstruction of the SrTiO₃ (100), leading to different low energy interface states.
 EDS analysis of the sub-oxide layer was conducted using SrTiO₃ (100) as an internal standard. K factors were determined from intensity ratios. EDS measurements from the sub-oxide revealed that there is no Ti, however a small amount of Ti was found in the Pt particle. This indicates that Ti diffuses into the Pt during dewetting and a sub-oxide (free from Ti) is created by reduction. The sub-oxide was found to be SrO_x (x=3-4), but because of a large standard deviation (probably due to the effect of specimen thickness) EELS analysis needs to be done to determine the exact concentration.
- Fig. 7b shows that under high oxygen partial pressure no surface reduction occurred. However, the Pt particle shape is not fully faceted, it can be correlate to oxygen adsorption which may influence the anisotropy of surface energies.

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